



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Gilbert Wolrich et al.
Serial No.: 10/057,738
Filed : January 25, 2002
Assignee : Intel Corporation
Title : DATA TRANSFER MECHANISM

Art Unit: 2189
Examiner: Shawn X. Gu

MAIL STOP RCE

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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Applicants call attention to the attached Information Disclosure Statement and documents listed on form PTO-1449.

This filing is being made with a Request for Continued Examination (RCE). No fee is required.

The documents are in the English language; hence no concise explanation is necessary per Rule 98(a)(3).

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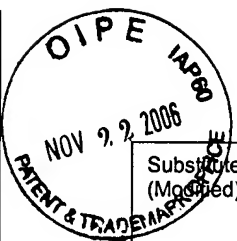
Respectfully submitted,

Date: 11/20/2006

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Substitute Form PTO-1449
(Modified)U.S. Department of Commerce
Patent and Trademark OfficeAttorney's Docket No.
10559-618001Application No.
10/057,738**Information Disclosure Statement
by Applicant**

(Use several sheets if necessary)

(37 CFR §1.98(b))

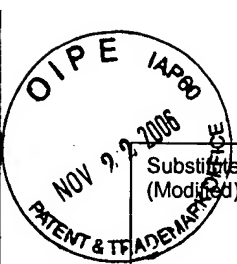
Applicant
Gilbert Wolrich et al.Filing Date
January 25, 2002Group Art Unit
2189**U.S. Patent Documents**

Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	AA	2002/0053017	05/2002	Adiletta et al.			
	AB	2002/0056037	05/2002	Wolrich et al.			
	AC	2003/0041228	02/2003	Rosenbluth et al.			
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	BA	5,481,683	01/1996	Karim			

Examiner Signature

Date Considered

EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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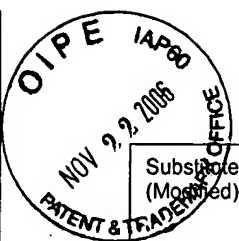
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2189**U.S. Patent Documents**

Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
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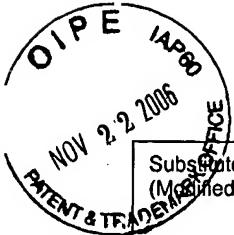


Substitute Form PTO-1449 (Modified)	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 10559-618001	Application No. 10/057,738
Information Disclosure Statement by Applicant (Use several sheets if necessary) (37 CFR §1.98(b))		Applicant Gilbert Wolrich et al.	
		Filing Date January 25, 2002	Group Art Unit 2189

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							Yes	No
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	CU	WO 00/33195	06/2000	WIPO				
	CV	WO 01/16697	03/2001	WIPO				
	CW	WO 01/16698	03/2001	WIPO				
	CX	WO 01/16702	03/2001	WIPO				
	CY	WO 01/16703	03/2001	WIPO				
	CZ	WO 01/16713	03/2001	WIPO				

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Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
							Yes	No
	DA	WO 01/16714	03/2001	WIPO				
	DB	WO 01/16715	03/2001	WIPO				
	DC	WO 01/16716	03/2001	WIPO				
	DD	WO 01/16718	03/2001	WIPO				
	DE	WO 01/16722	03/2001	WIPO				
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	DH	WO 01/41530	06/2001	WIPO				
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	DK	Chang et al., "Branch Classification: A New Mechanism For Improving Branch Predictor Performance," IEEE, pp. 22-31 (1994).
	DL	Farkas et al., "The multicluster architecture: reducing cycle time through partitioning," IEEE, vol. 30, December 1997, pp. 149-159.
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	DN	Keckler et al., "Exploiting fine grain thread level parallelism on the MIT multi-ALU processor," IEEE, June 1998.
	DO	Mendelson A. et al., "Design Alternatives of Multithreaded Architecture", <i>International Journal of Parallel Programming</i> , Plenum Press, New York, 27(3):161-193, June 1999.
	DP	Wadler, "The Concatenate Vanishes," University of Glasgow, December 1987 (revised November 1989), pp. 1-7.
	DQ	Waldspurger et al., "Register Relocation: Flexible Contents for Multithreading," Proceedings of the 20th Annual International Symposium on Computer Architecture, 1993, pp. 120-130.

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